FIG. 12

FRAMI	E 0	33 BLOCKS		-		
8 / PIXELSγ	8 PIXELS \		8 /PIXELS y	8 PIXELS (
A1	A2		A32	A33	8 PIXELS	
B1	B2		B32	B33	8 PIXELS	
C1	C2		C32	C33	8 PIXELS	
A34	A35		A65	A66	8 PIXELS	
B34	B35		B65	B66	8 PIXELS	30
C34	C35		C65	C66	8 PIXELS	BLOCKS
-	- -	- - -	-	- - -		
A298	A299		A329	A330	8 PIXELS	
B298	B299		B329	B330	8 PIXELS	
C298	C299		C329	C330	8 PIXELS	

FIG. 13

FRAM	E 1	—— 33 BLOCKS ——				
8 PIXELS	8 PIXELS \		8 /PIXELS	8 YPIXELS (
A1	A2		A32	A33	8 PIXELS	
B1	B2		B32	B33	8 PIXELS	
C1	C2		C32	C33	8 PIXELS	
A34	A35 ·		A65	A66	8 PIXELS	
B34	B35		B65	B66	8 PIXELS	30
C34	C35		C65	C66	8 PIXELS	BLOCKS
-	-	-		-		
A298	A299		A329	A330	8 PIXELS	
B298	B299 _.		B329	B330	8 PIXELS	
C298	C299		C329	C330	8 PIXELS	

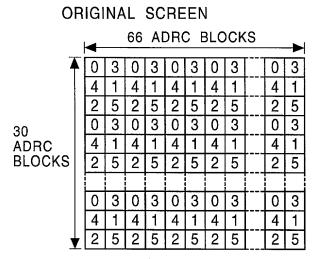
FIG. 14

		-			
ADRC BLOCK 5					
ADRC BLOCK					
ADRC BLOCK					
ADRC BLOCK					

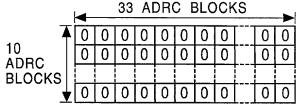
.

FIG. 15A

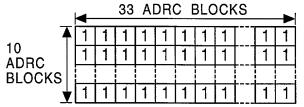
FIG. 15B



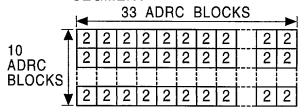
SEGMENT



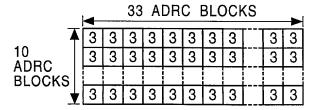
SEGMENT



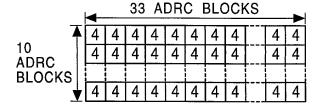
SEGMENT



SEGMENT



SEGMENT



SEGMENT

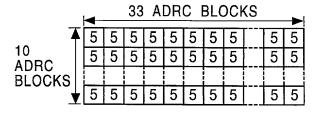
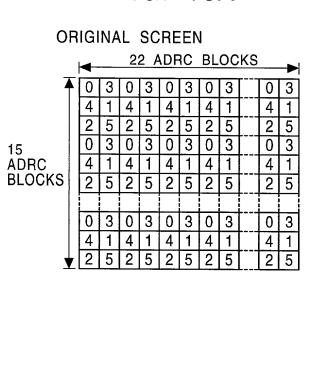
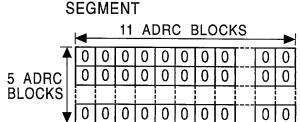
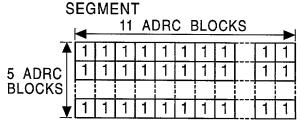


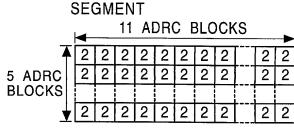
FIG. 16A

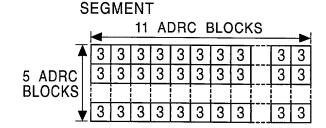
FIG. 16B

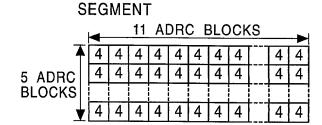












5	SEG	ME	EN٦	_							
	—		11	ΑI	OR	C E	3LC	Ck	S		->
1	5	5	5	5	5	5	5	5	ļ	5	5
5 ADRC	5	5	5	5	5	5	5	5		5	5
BLOCKS											
	5	5	5	5	5	5	5	5		5	5

FIG. 17A

y32	, y65	y329
у4	y37	y301
у3	y36	y300
у2	y35	y299
y1	y34	y298
y0	y33	y297

FIG. 17B

0 5	y221	y112	у3	y224		y142
733	y254	y 145	736)52/		<u>e</u>
				 	!	
y297	y188	y79	y300	y191	_	V109

FIG. 17D

FIG. 17C

144 n	88n	0n
0 9 n	68n	9n
12n	n40	/n
n52	u41	8n
n53	u42	6n
u54	u43	 u10

u54

u47

n46

u45

n44

u21

u15

u4

u3 u14

u2

9

FIG. 17E

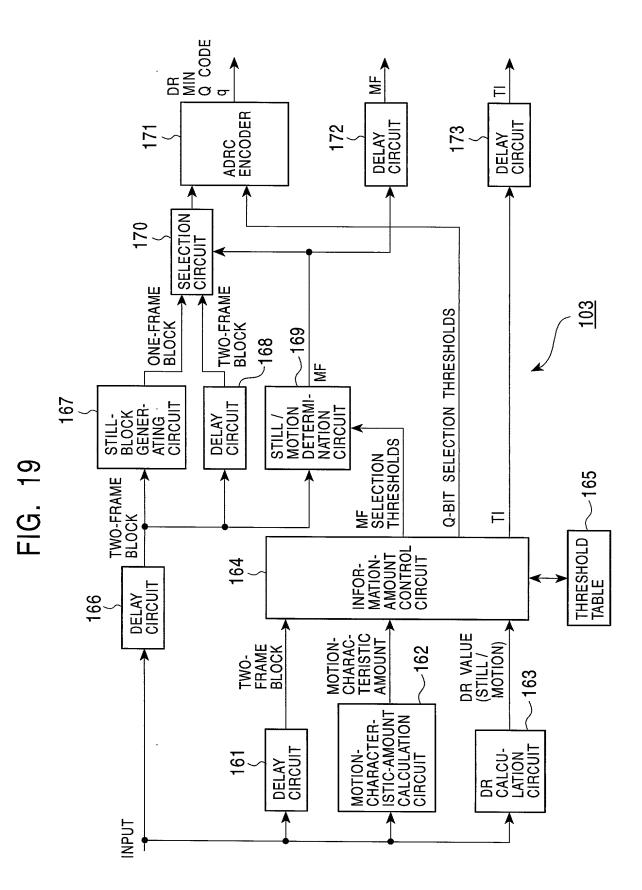
٧47	۷46	

FIG. 17F

			
۷44	٧33		۸0
۸50	68۸		9/
۸51	۸40		٧7
v52	٧41		۸8
٧53	٧42		6^
٧54	٧43		٧10

FIG. 18

SEG	SEGMENT 0	S	SEGMENT	⊢	SEGI	SEGMENT 2		SEGMENT 3	SEGN	SEGMENT 4	SEGMENT 5
				; ;		 	! ! ! !	 	 - - - -	 	
9	y221	۷112	u54	у3	u54 y3 y224 y115 v54 y6	y115	v54	y6	y109 v0	0/	



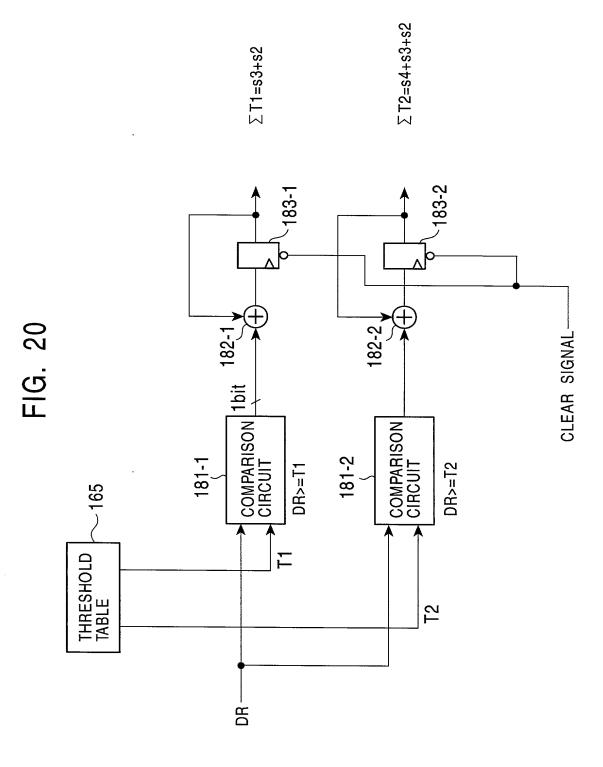
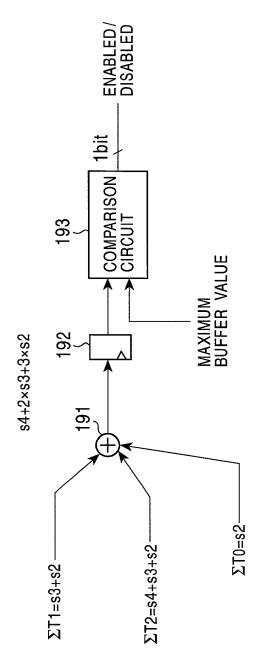


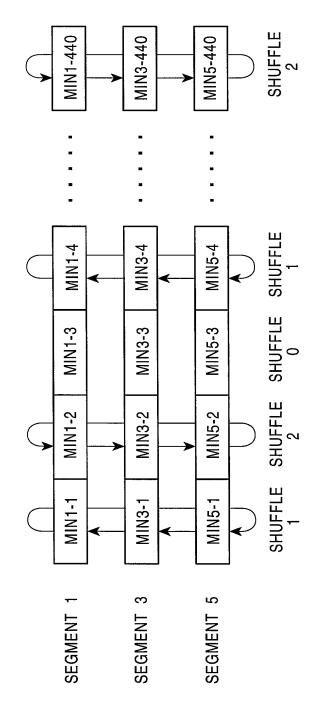
FIG. 21



SEGMENT 0 (1) (5) SEGMENT 2 (3) SHUFFLE 2 SEGMENT 4 SEGMENT 0 (1) SEGMENT 2 (3) SEGMENT 4 (5) FIG. 22 SHUFFLE 1 SEGMENT 0 (1) (9) SEGMENT 4 (5) SHUFFLE 0 SEGMENT 2

DR5-440 **DR1-440** SHUFFLE 1 DR3-440 DR3-4 DR1-4 DR5-4 FIG. 23 SHUFFLE 2 DR1-3 DR3-3 DR5-3 SHUFFLE 1 DR1-2 DR3-2 DR5-2 DR5-1 DR1-1 DR3-1 SEGMENT 3 SEGMENT 5 SEGMENT 1

FIG. 24



SHUFFLE 0 MF5-440 MF1-440 MF3-440 SHUFFLE SHUFFLE SHUFFLE 0 2 MF1-4 MF3-4 MF5-4 FIG. 25 MF1-3 MF3-3 MF5-3 MF1-2 MF3-2 MF5-2 SHUFFLE 2 MF1-1 MF3-1 MF5-1 SEGMENT 3 SEGMENT 5 SEGMENT 1

,

FIG. 26

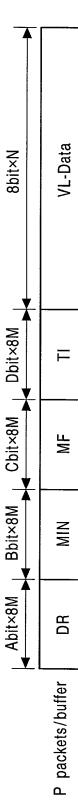
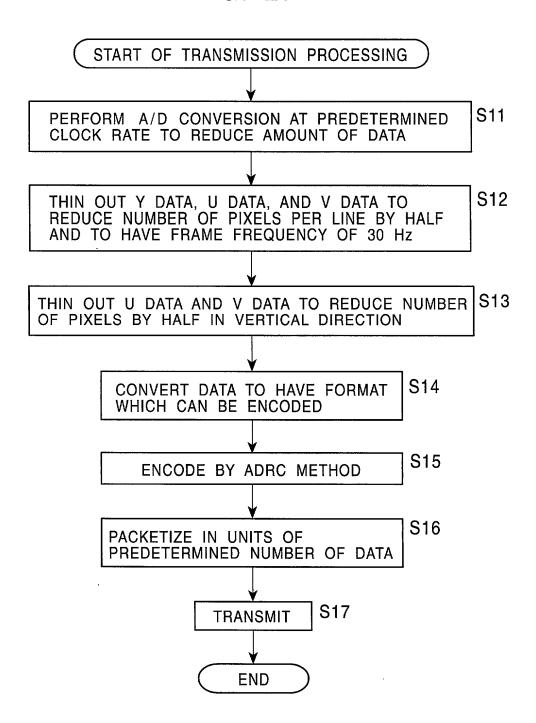
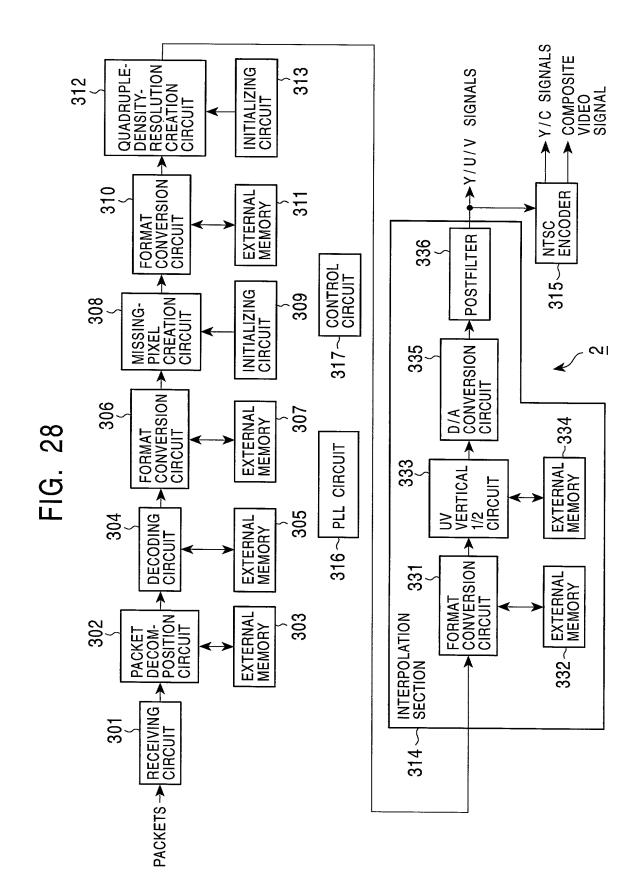


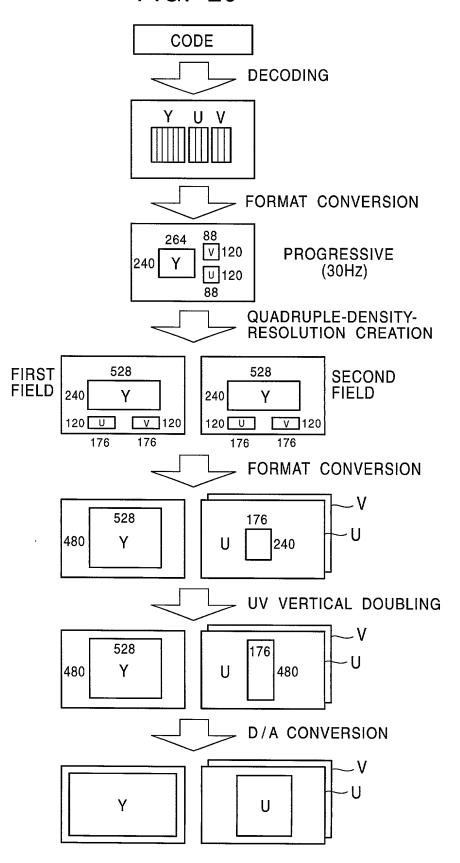
FIG. 27





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FIG. 29



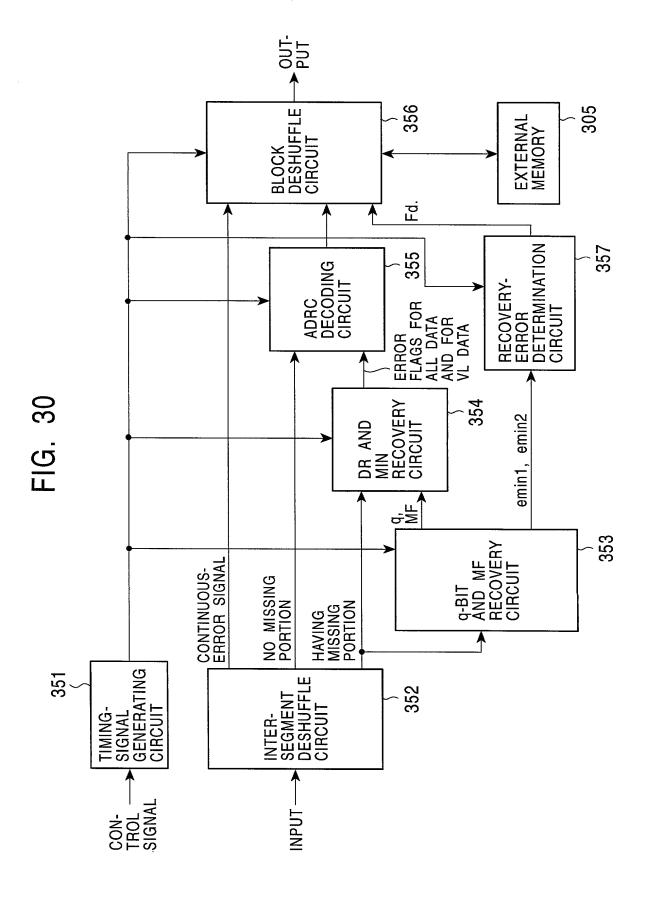


FIG. 31

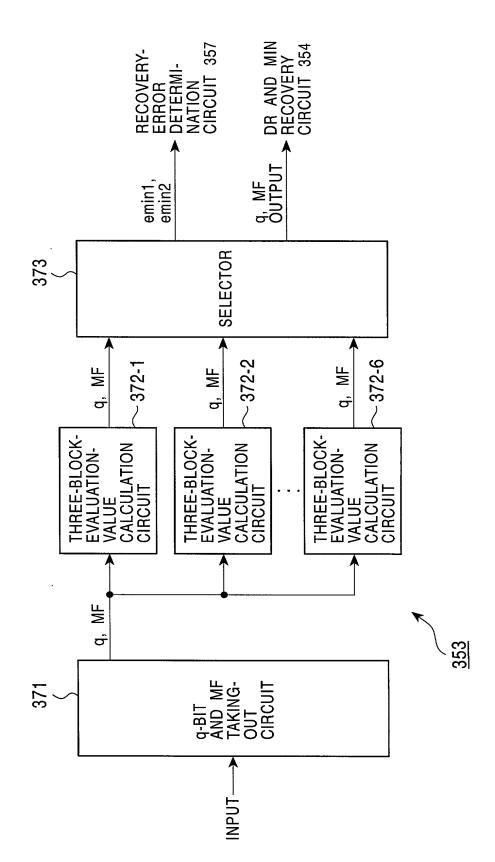


FIG. 32

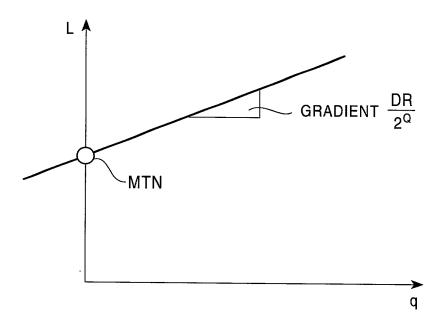


FIG. 33

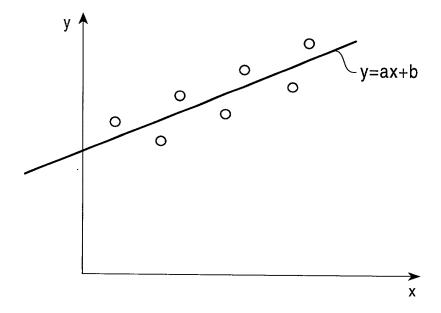


FIG. 34A

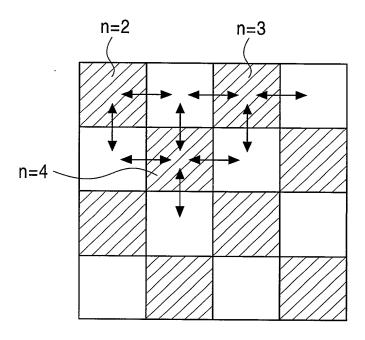


FIG. 34B

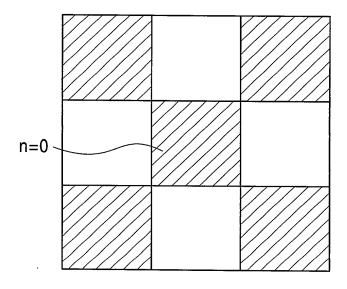


FIG. 35

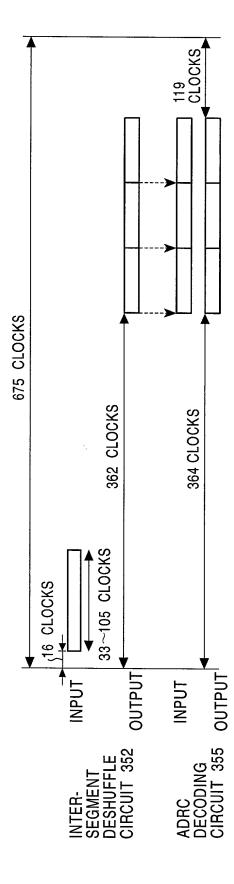


FIG. 36

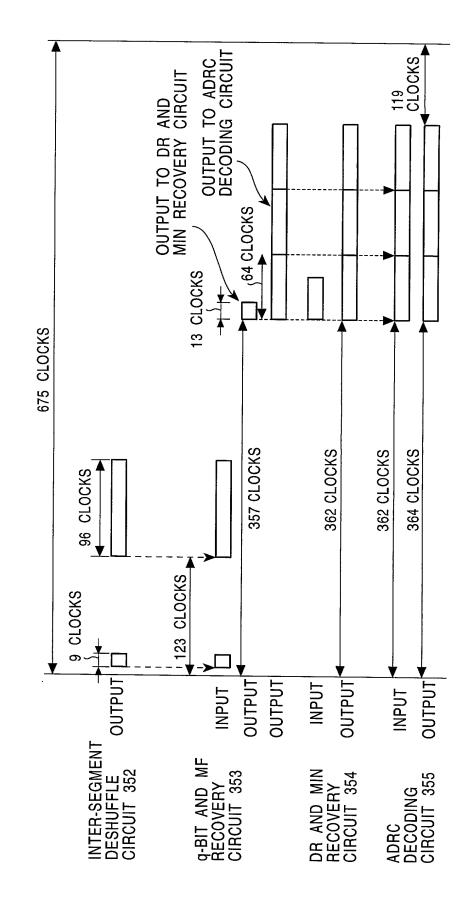


FIG. 37A

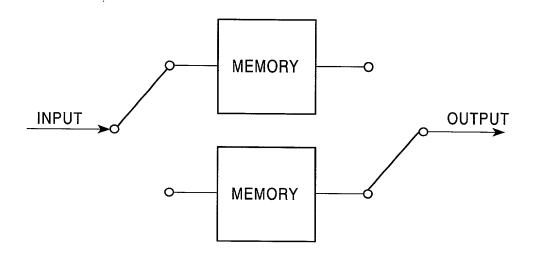


FIG. 37B

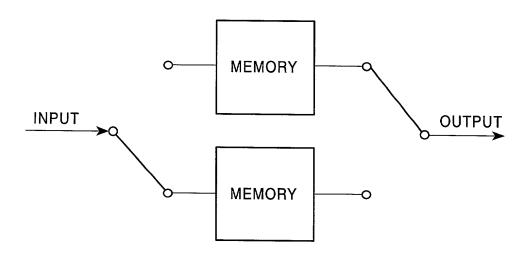


FIG. 38A

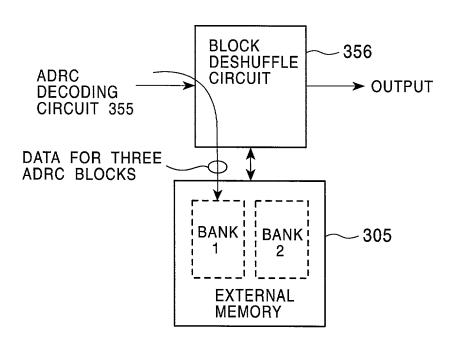


FIG. 38B

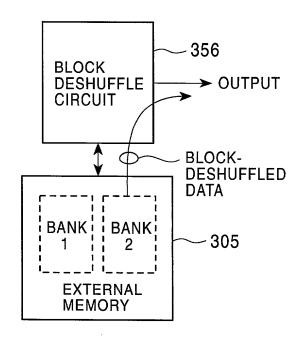


FIG. 39

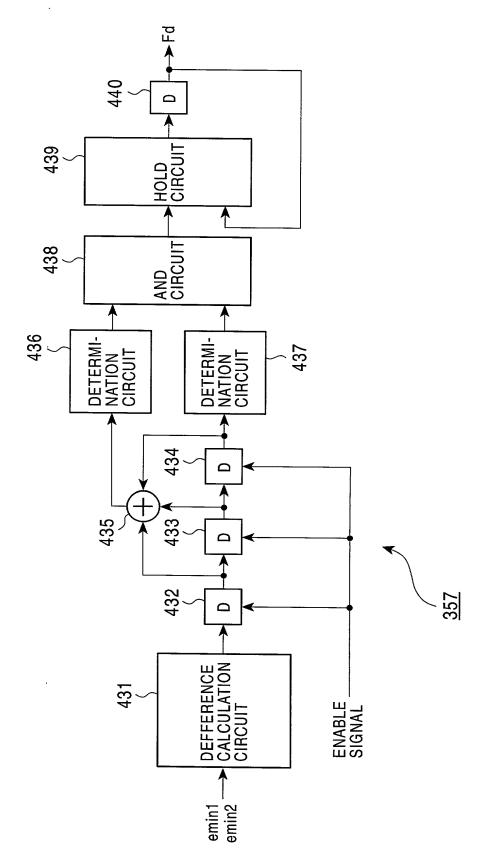
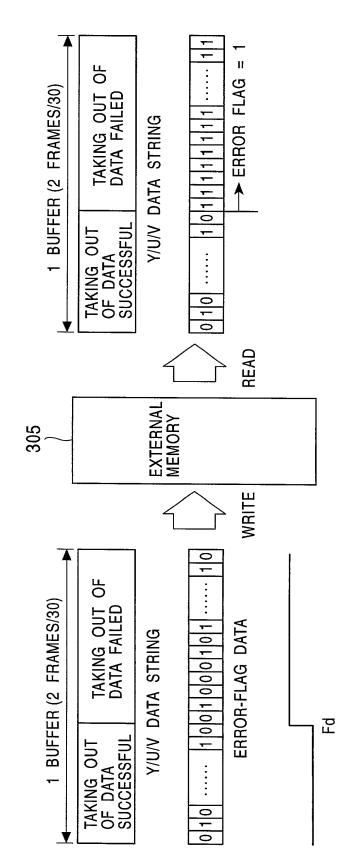
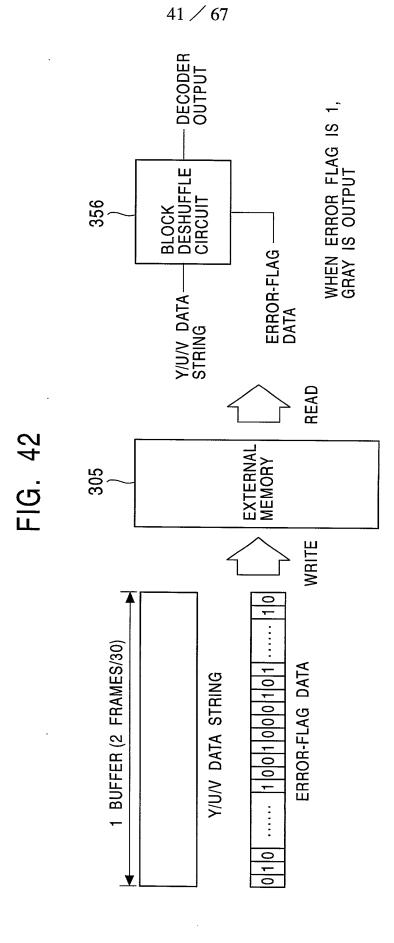


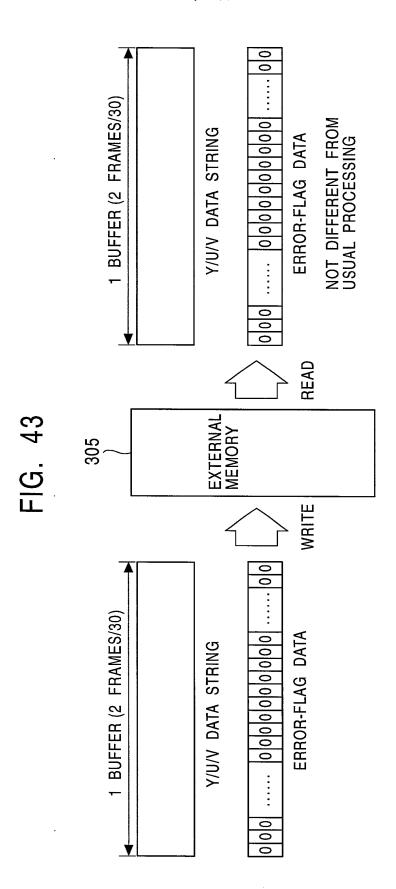
FIG. 40



Y/C DATA OBTAINED FOUR FRAMES BEFORE → ERROR FLAG = 1 1 BUFFER (2 FRAMES/30) Y/U/V DATA STRING 101111111 TAKING OUT OF DATA SUCCESSFUL READ EXTERNAL MEMORY 305 WRITE TAKING OUT OF DATA FAILED CONTINUOUS-ERROR SIGNAL 1 BUFFER (2 FRAMES/30) ERROR-FLAG DATA 1001000101 Y/U/V DATA STRING TAKING OUT OF DATA SUCCESSFUL 010

FIG. 41





1 BUFFER (2 FRAMES/30) Y/C DATA OBTAINED FOUR FRAMES BEFORE SET ALL ERROR FLAGS IN ONE BUFFER TO 1 1111111111 Y/U/V DATA STRING READ EXTERNAL MEMORY FIG. 44 305 WRITING OF Y/C DATA NOT PERFORMED 1 BUFFER (2 FRAMES/30) ERROR-FLAG DATA 10011001101 Y/U/V DATA STRING 000

FIG. 45

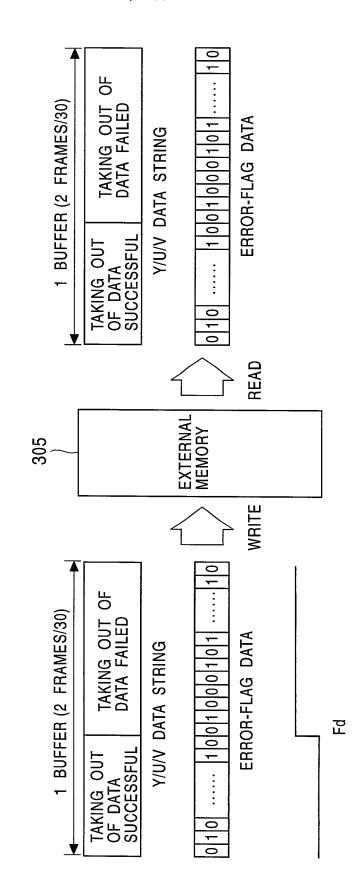


FIG. 46

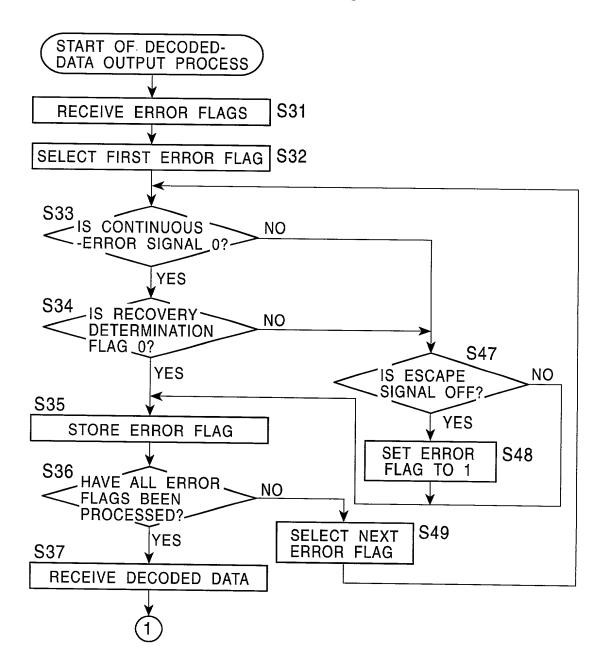


FIG. 47

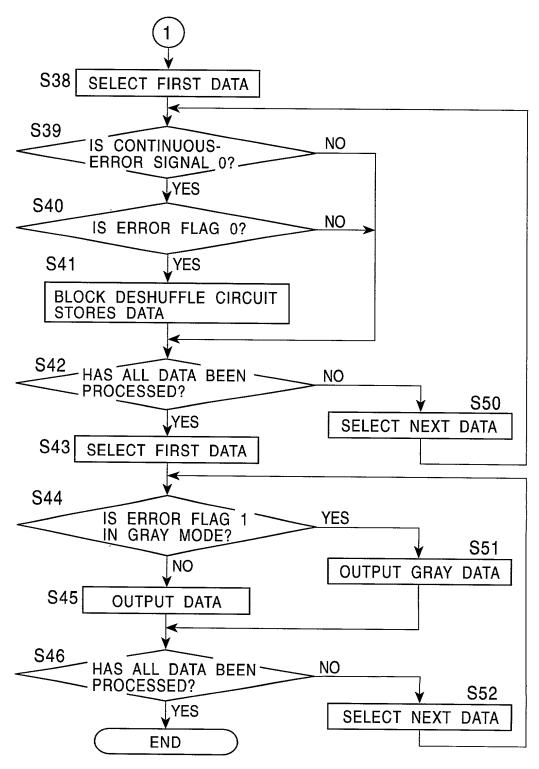


FIG. 48

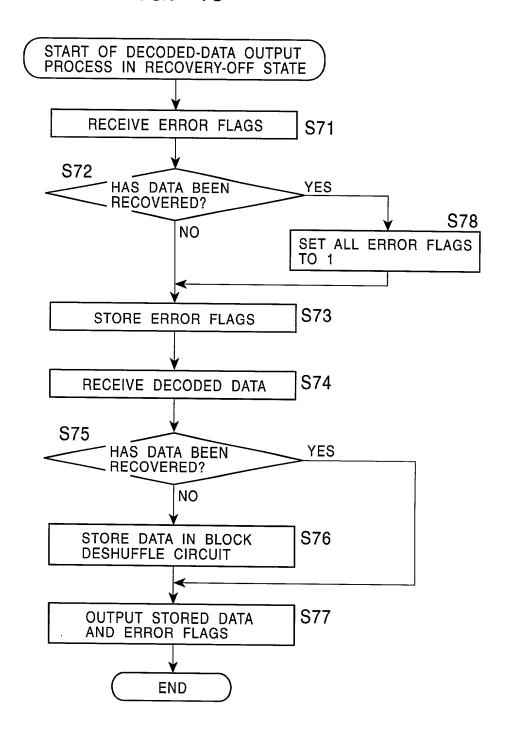


FIG. 49A

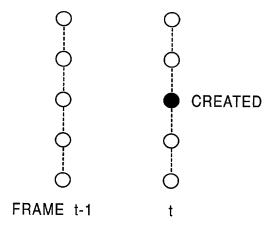


FIG. 49B

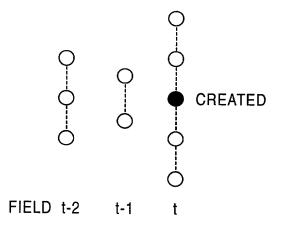


FIG. 51

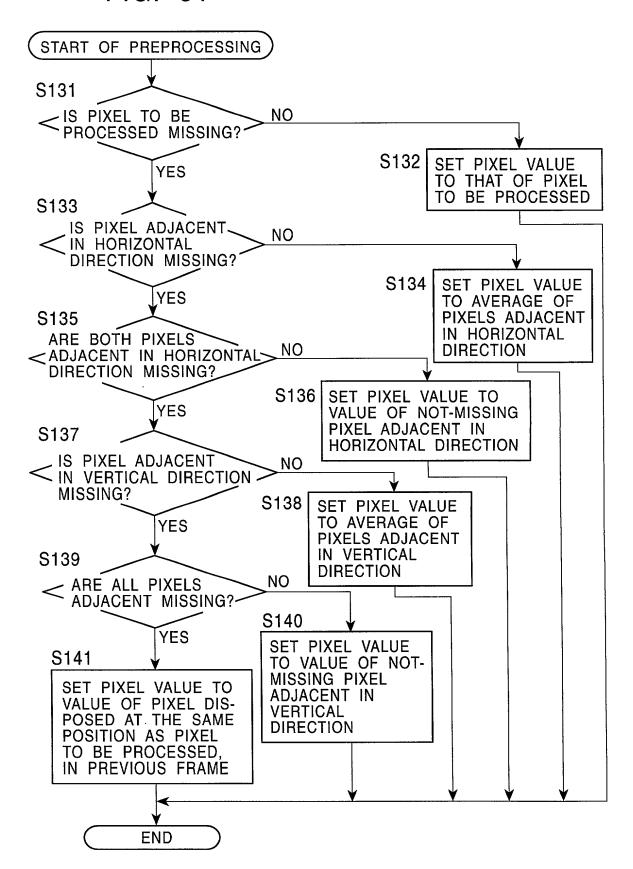
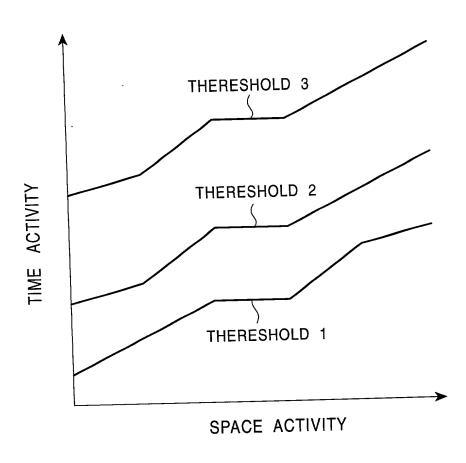


	FIG.	52A			FIG.	52B	
L1 L2 L3	H1 ERROR q4 q7	H2 q2 ERROR ERROR	H3 q3 q6 q9	L1 L2 L3	•	H2 p2 p5 p8	H3 p3 p6 p9
	FRA	ME t			FRA	ME t-1	

FIG. 53

q1 q2 q3 q4 ERROR q6 q7 q8 q9 FRAME t

FIG. 54



HACTALL

FIG. 55

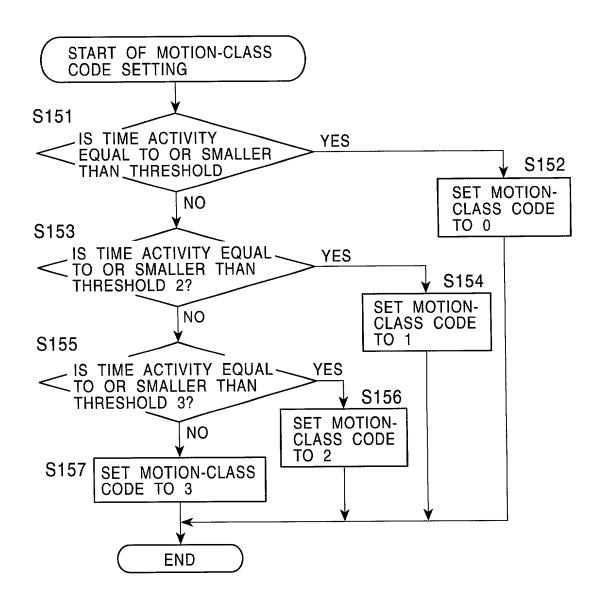


FIG. 56

L-2	\bigcirc	\bigcirc	\bigcirc
L-1	\bigcirc	\bigcirc	\bigcirc
LO	\bigcirc	\oslash	\bigcirc
L+1	\bigcirc	\bigcirc	\bigcirc
L+2	\bigcirc		\bigcirc

: PIXEL TO BE PROCESSED

FIG. 57

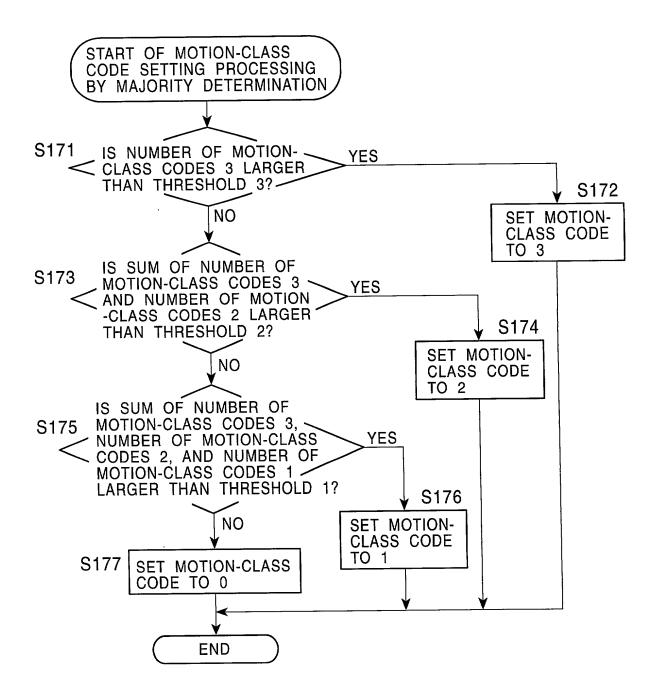


FIG. 58

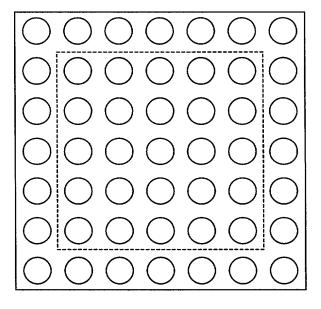


FIG. 59

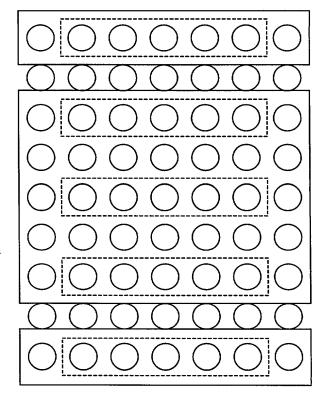


FIG. 60

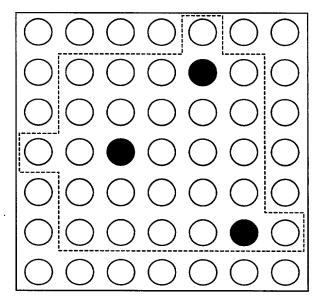


FIG. 61

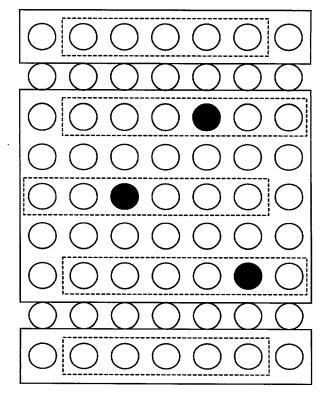


FIG. 62A

FIG. 62C

INTERLACE

PROGRESSIVE

FIRST
FIELD

CREATED

TIME
FRAME t-1

FIG. 62B

INTERLACE

PROGRESSIVE

SECOND
FIELD

CREATED

TIME

FRAME t-1

t

FRAME t-1

TIME

FRAME t-1

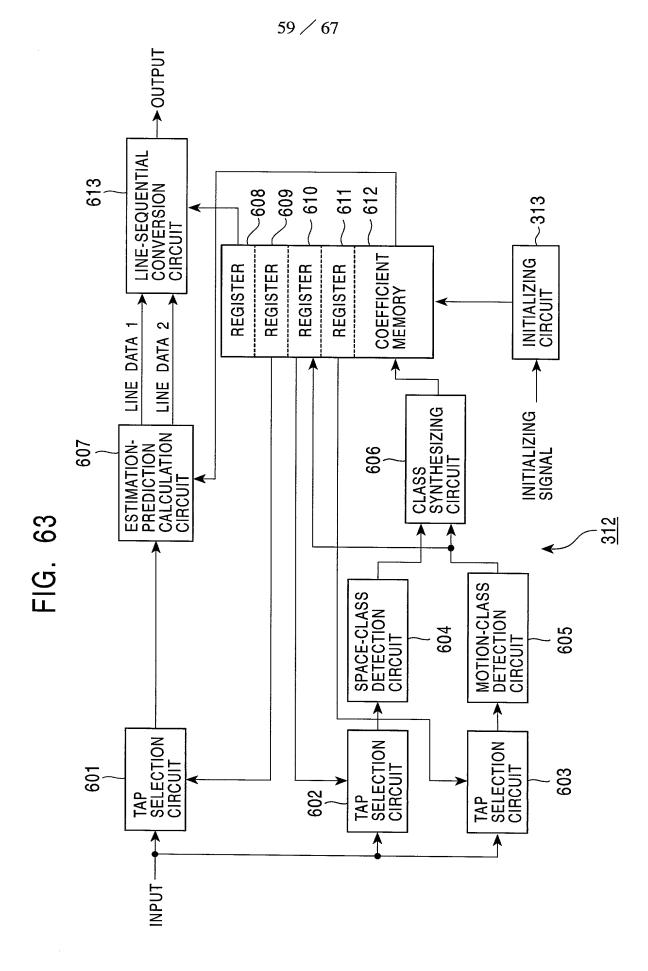


FIG. 64

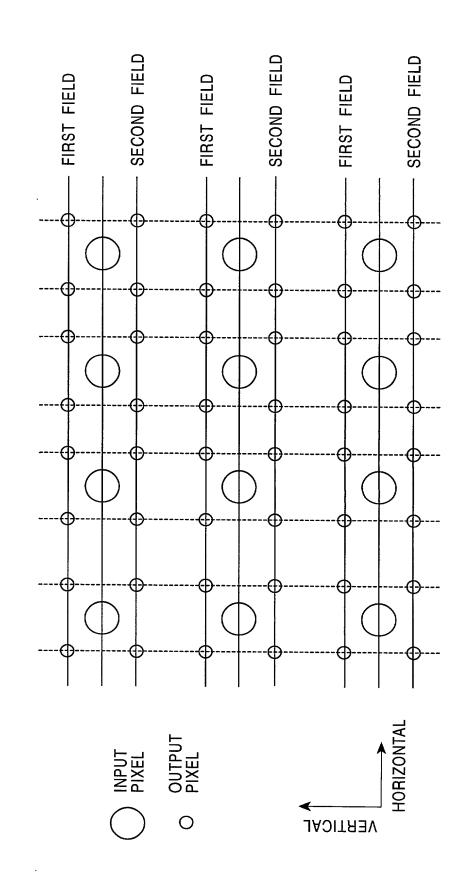


FIG. 65 HORIZONTAL OUTPUT PIXEL 0 VERTICAL

FIG. 66

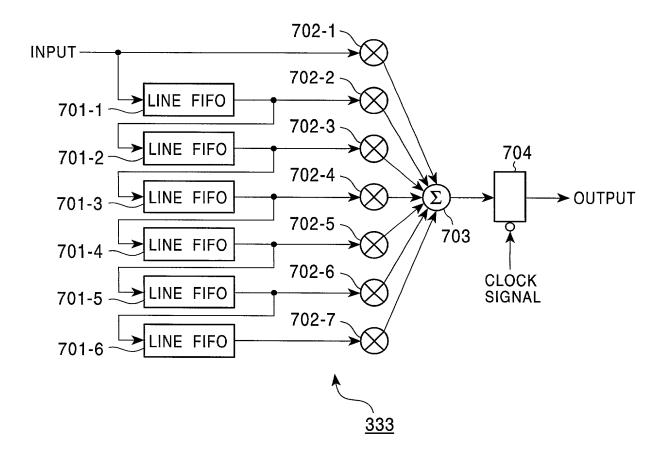


FIG. 67

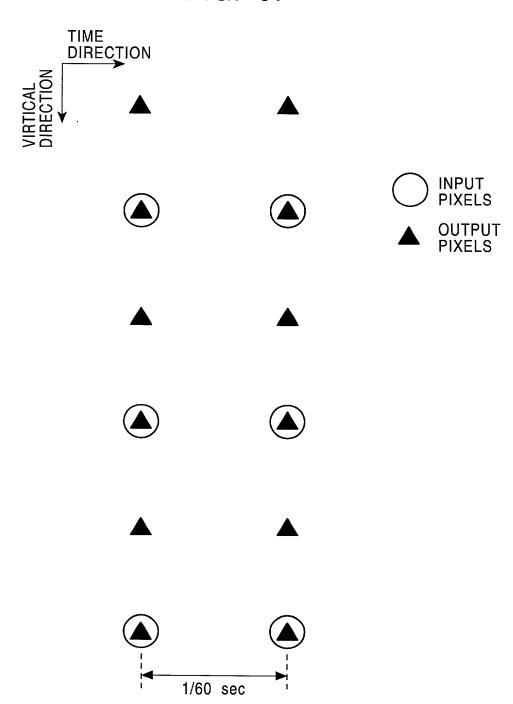


FIG. 68

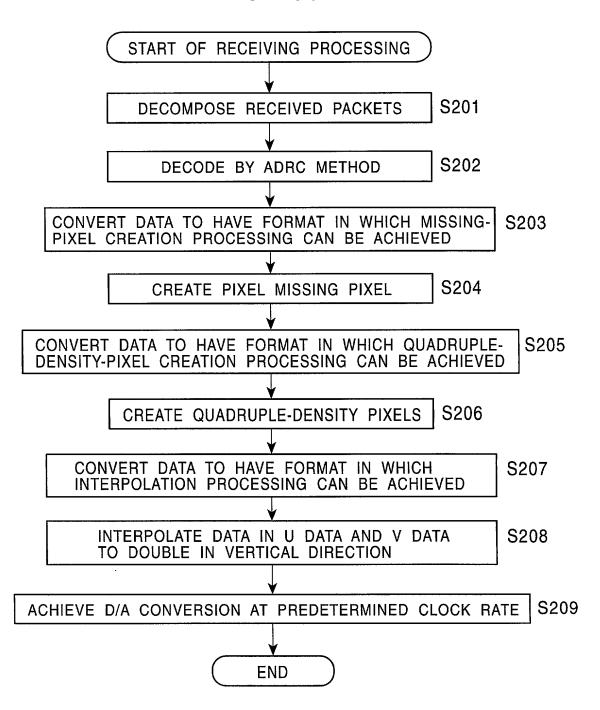


FIG. 69

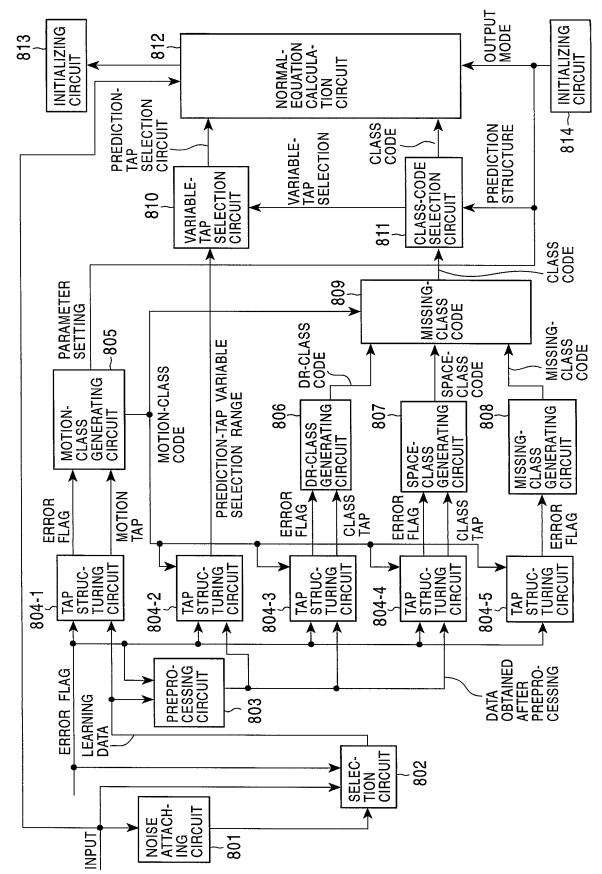


FIG. 70

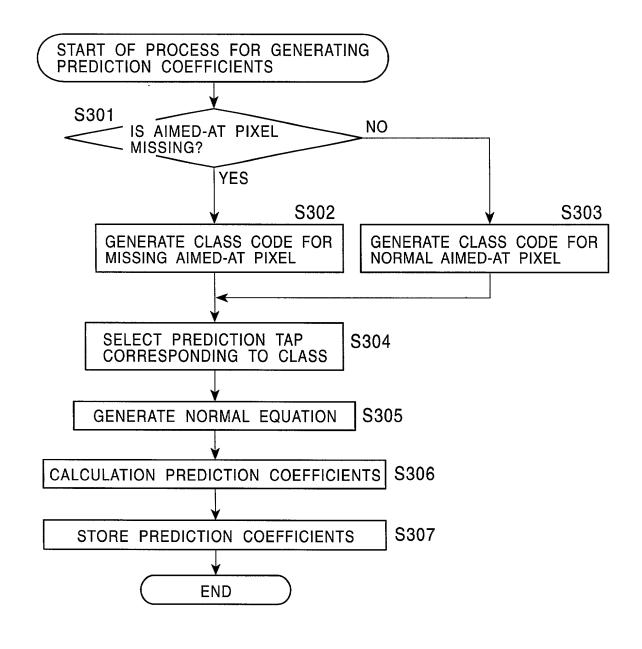


FIG. 71

